

ABSTRACT

Disclosed is an array substrate for a liquid crystal display, including: a substrate; a gate line and a thin film transistor having a gate electrode, a source electrode, a drain electrode and an active layer formed over the substrate; an interlayer insulating layer formed on the thin film transistor; a first gate redundancy line formed on the interlayer insulating layer, and connected electrically with one of the gate electrode and the gate line through a first gate contact hole and formed of the same material as the source and drain electrodes; a passivation layer provided on the first gate redundancy line and the interlayer insulating layer; and a pixel electrode electrically connected with the drain electrode through the drain contact hole formed in the passivation layer.